

## **Antonio G. M. Strollo**

### *Education*

1988 = Laurea Degree (cum laude) in Electronic Engineering – University Napoli Federico II (Italy)

1992 = PhD in Electronic Engineering and Computer Science – University Napoli Federico II (Italy)

### *Current Position*

2001 – present = Full Professor at University Napoli Federico II (Italy) – Dept. of Electrical Engineering and Information Technology

### *Previous Positions*

1990 – 1998 = Assistant Professor at University Napoli Federico II (Italy) - Dept. of Electronic Engineering

1998 – 2001 = Associate Professor – same university and dept.

### *Teaching Activities*

1994 – Present = Academic regular courses in VLSI Design, Digital electronics, Analog Electronics and Fundamental of Electronics, University Napoli (Italy)

2010, 2019 = Analog Electronics, Air Force Academy of Pozzuoli (Italy)

2001, 2002, 2010 = Advanced courses at STMicroelectronics (Italy) and Ansaldo Trasporti (Italy)

### *Other Professional Activities*

2009-2012 = Associate Editor - IEEE Transactions on Circuits and Systems I

2011 – Present = Associate Editor – Integration, the VLSI Journal

2011 – Present = Rector Delegate and president of the "NeaPolis Innovation", a joint initiative involving the Universities of Campania region, STMicroelectronics, Micron, ENEA, CNR and Campania Industrial Association, aimed to develop research, teaching and technology transfer in the field of Electronics

### *Institutional Responsibilities*

2005 – 2008 = Head of the Department of Electronic and Telecommunication Engineering of the University of Napoli Federico II (Italy)

2005 – 2012 = Member of the management board of "Polo delle Scienze e Tecnologie" - same University.

2012 – 2019 = Coordinator of the bachelor and master courses in Electronic Engineering - same University.

### *Membership of Scientific Societies*

IEEE: Member (2005) – Senior member (2006)

### *Grants*

1998 – Present = Research projects funded by STMicroelectronics, Numonyx, International Rectifiers, Gavazzi Space, Techno System Developments, Micron.

2001 – 2002 = PRIN "Advanced power devices with local lifetime control" PI

2003 – 2005 = PRIN "A new architecture for RF frequency synthesizer for GFSK/GMSK modulation" Naples University coordinator.

2011 – 2013 = Scientific Responsible of the "Laboratorio della Campania per lo studio della microelettronica", a joined research centre between STMicroelectronics and the Universities of Napoli Federico and Salerno, funded by STM for more than 600.000 Euro.

2015 – Present = PRIN "Advanced Nanometer IC Technologies for Next Generation Transceivers (ANiThiNG)" Naples University coordinator.

2019 – Present = PRIN "Autonomous In-vivo Brain-Machine-Interface in 28nm-CMOS technology with Ultrasound-based Power-Harvester and Communication-Link (Brain28nm)" Naples University coordinator.